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a pre-charging circuit for charging the plurality of data signal lines to a pre-charging voltage in a predetermined period of time in accordance with a pre-charging control signal,

wherein a pre-charging voltage stabilizing section for suppressing fluctuation in said pre-charging voltage is provided on a preceding stage of said pre-charging circuit.

2. The image display device as set forth in claim 1,
wherein:

said pre-charging voltage stabilizing section is
composed of current controlling means and charge holding
means.

3. The image display device as set forth in claim 2,
wherein:

said charge holding means is composed of a
capacitor.

4. The image display device as set forth in claim 2,
wherein:

said current controlling means is composed of a
resistor.

5. The image display device as set forth in claim 3,
wherein:

said charge holding means has a capacitance which is
at least larger than a total capacitance of said
plurality of data signal lines.

6. The image display device as set forth in claim 2,
wherein:

when said pre-charging voltage is an AC voltage,

time constants of said current controlling means and charge holding means making up said pre-charging voltage stabilizing section are shorter than a duration of time from switching of polarity of said pre-charging voltage to next activation of said pre-charge control signal.

7. The image display device as set forth in claim 2, wherein:

when said pre-charging voltage is an AC voltage, time constants of said current controlling means and charge holding means making up said pre-charging voltage stabilizing section are shorter than a duration of time from activation of said pre-charging control signal to next activation thereof.

8. The image display device as set forth in claim 2, wherein:

when said pre-charging voltage is an AC voltage, said current controlling means and charge holding means making up said pre-charging voltage stabilizing section are arranged to stabilize said pre-charging voltage to a sufficient potential within a period of said pre-charging control signal.

9. The image display device as set forth in claim 2,

wherein:

10. The image display device as set forth in claim 2,
wherein:

11. A driving method of an image display device, said device comprising:

a display section, having a plurality of data signal lines for respective columns of the plurality of pixels and having a plurality of scanning signal lines corresponding to respective rows of the plurality of

13. The driving method of the image display device as set forth in claim 11, wherein:

14. A driving method of an image display device, said device comprising:

a display section, having a plurality of data signal lines for respective columns of the plurality of pixels and having a plurality of scanning signal lines corresponding to respective rows of the plurality of pixels, for displaying an image by writing a video signal from each data signal line into each pixel in synchronism with a scanning signal which is outputted from each scanning signal line;

a scanning signal line driving circuit for outputting said scanning signal to said plurality of scanning signal lines; and

a pre-charging circuit for charging said plurality

said driving method comprising the step of supplying said pre-charging voltage to a pre-charging voltage stabilizing section which is composed of current controlling means and charge holding means and is provided on a preceding stage of said pre-charging circuit, said pre-charging voltage having a DC voltage in synchronism with one horizontal period of said video signal.

said pre-charging voltage has a DC voltage which is equipotential from maximum amplification values of positive and negative polarities of said video signal.

a pre-charging circuit for charging said data signal lines by supplying a pre-charging voltage based on a pre-charging control signal within a predetermined period

a pre-charging voltage stabilizing circuit for stabilizing said pre-charging voltage so as to supply charges to said pre-charging circuit by said pre-charging voltage, the charges being not less than an amount of charges which was supplied to said data signal lines by said pre-charging circuit.

said pre-charging voltage/stabilizing circuit includes serially connected resistor and capacitor, and said capacitor supplies said pre-charging circuit with said charges.

when said pre-charging voltage is an AC voltage, time constants according to said resistor and capacitor are shorter than a duration of time from switching of polarity of said pre-charging voltage to next activation of said pre-charging control signal.

19. The image display device as set forth in claim 17,

wherein:

20. The image display device as set forth in claim 17,
wherein:

21. A driving method of an image display device, said device comprising:

from each scanning signal line;

a scanning signal line driving circuit for outputting a scanning signal to the plurality of scanning signal lines by a pulse width control signal which controls an output signal in synchronism with a scanning start signal and a scanning timing signal, and a signal width of the output signal;

pre-charging voltage stabilizing means for stabilizing a pre-charging voltage from the pre-charging circuit; and

said driving method comprising the step of suspending a scanning signal for a predetermined period of time when a pre-charging voltage stabilizing circuit having charge holding means and current controlling means as the pre-charging voltage stabilizing means is used to

22. The driving method of the image display device as set forth in claim 21, wherein:

23. The driving method of the image display device as set forth in claim 21, wherein:

input of the scanning start signal and the scanning timing signal to the scanning signal line driving circuit is suspended for the predetermined period of time so as to suspend the scanning signal.

24. The driving method of the image display device as set forth in claim 21, wherein:

input of the scanning timing signal to the scanning signal line driving circuit and the pulse width control signal are suspended for the predetermined period of time

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28. The driving method of the image display device as set forth in claim 24, wherein:

32. The driving method of the image display device as set forth in claim 24, wherein:

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ired for sufficiently stabilizing the voltage.

age display device, comprising:

- plurality of pixels disposed in a matrix display section, having a plurality of respective columns of the plurality;
- a plurality of scanning signals applied to respective rows of the pixels for displaying an image by supplying from each data signal line to each pixel with a scanning signal which is applied to a scanning signal line;
- a signal line driving circuit for driving a signal to the plurality of data signal lines with a predetermined timing signal;
- a scanning signal line driving circuit for applying a scanning signal to the plurality of scanning lines by a pulse width control signal;
- an output signal in synchronism with the scanning signal and a scanning timing signal, and a scanning timing signal, and a scanning timing signal;
- the output signal;
- a pre-charging circuit for charging the data signal lines to a pre-charging voltage in response to a pre-charging control signal in a predetermined timing;

37. An image display device, comprising:
- a plurality of pixels disposed in a matrix;
 - a display section, having a plurality of data signal lines for respective columns of the plurality of pixels and having a plurality of scanning signal lines corresponding to respective rows of the plurality of pixels, for displaying an image by supplying a video signal from each data signal line to each pixel in correspondence with a scanning signal which is supplied from each scanning signal line;
 - a data signal line driving circuit for outputting a video signal to the plurality of data signal lines in synchronism with a predetermined timing signal;
 - a scanning signal line driving circuit for outputting a scanning signal to the plurality of scanning signal lines by a pulse width control signal which controls an output signal in synchronism with a scanning start signal and a scanning timing signal, and a signal width of the output signal;
 - a pre-charging circuit for charging the plurality of data signal lines to a pre-charging voltage by a pre-charging control signal in a predetermined period of

pre-charging voltage stabilizing means for stabilizing a pre-charging voltage from the pre-charging circuit; and

wherein:

in a non-match image display mode, one portion of the display section is set as a video data non-display area which avoids display of video data,

a control signal generating section for suspending a scanning signal for a predetermined period of time is provided in the control signal generating circuit, when performing display at fixed brightness in the video data non-display area by a pre-charging voltage inputted from the pre-charging circuit.

38. The image display device as set forth in claim 37,
wherein the predetermined period of time is not less than
a time constant of current controlling means and charge

39. The image display device as set forth in claim 37, wherein the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are formed on a single substrate.

40. The image display device as set forth in claim 38, wherein the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are formed on a single substrate.

41. The image display device as set forth in claim 37,
wherein a switching element composing the pre-charging
circuit, the data signal line driving circuit, the
scanning signal line driving circuit and the pixels is
made of a polycrystalline silicon thin film transistor.

42. The image display device as set forth in claim 38, wherein a switching element composing the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels is made of a polycrystalline silicon thin film transistor.

44. The image display device as set forth in claim 40, wherein a switching element composing the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels is made of a polycrystalline silicon thin film transistor.

46. The image display device as set forth in claim 38, wherein switching elements which respectively form the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are manufactured at a process temperature of not more than 600 °C.

48. The image display device as set forth in claim 40, wherein switching elements which respectively form the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are manufactured at a process temperature of not more than 600 °C.

49. The image display device/as set forth in claim 41, wherein switching elements which respectively form the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are manufactured at a process temperature of not more than 600 °C.

50. The image display device as set forth in claim 42,
wherein switching elements which respectively form the
pre-charging circuit, the data signal line driving

51. The image display device as set forth in claim 43, wherein switching elements which respectively form the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are manufactured at a process temperature of not more than 600 °C.

52. The image display device as set forth in claim 44, wherein switching elements which respectively form the pre-charging circuit, the data signal line driving circuit, the scanning signal line driving circuit and the pixels are manufactured at a process temperature of not more than 600 °C.